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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: **Abdellatif Bellaouar, et al.**

Docket No.: **TI-36131**

Serial No.: **10/810,095**

Examiner: **TBD**

Filed: **03/26/2004**

Art Unit: **2817**

For: **Low-Current, Area-Efficient and Flicker Noise
Free Bias CMOS Voltage Control Oscillator**

Conf. No.: **8199**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(A)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450 on

July 6, 2004
Elizabeth Austin
Elizabeth Austin

Dear Sir:

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. Copies of the listed references are enclosed.

This information disclosure statement is being mailed before receipt of a first office action on the merits in accordance with 37 C.F.R. 1.97(b)(3).

No fees are due, however, should the Office decide otherwise, please charge the necessary fee of \$180 to the deposit account of Texas Instruments Incorporated, Account Number 20-0668.

Respectfully submitted,

Ronald O. Neerings

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Texas Instruments Incorporated
P. O. Box 655474, MS 3999
Dallas, Texas 75265
(972) 917-5299

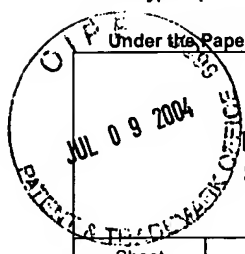
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PTO/SB/08A (08-00)

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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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Substitute for Form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete If Known

Application Number	10/810,095
Filing Date	03/26/2004
First Named Inventor	Abdellatif Bellaouar, et al.
Group Art Unit	2817
Examiner Name	TBD
Attorney Docket No.	TI-36131

Sheet	1	of	2
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U.S. PATENT DOCUMENTS

Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
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FOREIGN PATENT DOCUMENTS

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¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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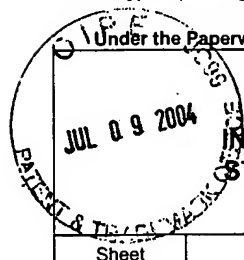
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STATEMENT BY APPLICANT**

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Group Art Unit	2817
Examiner Name	TBD
Attorney Docket No.	TI-36131

Sheet	2	of	2
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Exam. Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	CA	"A Filtering Technique to Lower Oscillator Phase Noise", Emad Hegazi, et al., 2001 IEEE International Solid-State Circuits Conference, ISSCC 2001/Session 23/Analog Techniques/23.4, pp. 364, 365, 465	
	CB	"A 50GHz VCO in 0.25µm CMOS", HongMo Wang, 2001 IEEE International Solid-State Circuits Conference, ISSCC 2001/Session 23/ Analog Techniques/23.8, pp. 372, 373	
	CC	"A Study of Phase Noise In CMOS Oscillators", Behzad Razavi, IEEE Journal of Solid-State Circuits, Vol. 31, No. 3, 03/1996, pp. 331-343	
	CD	"Electromagnetically Shielded High-Q CMOS-Compatible Copper Inductors", Hongsui Jlang, et al., IEEE International Solid-State Circuits Conference, ISSCC 2000/Session 19 / TD: High-Frequency Wireless / Paper WA 19.7, pp. 330-331	
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